

Figure 1

| A. ADD r1, r2, r3 | B. STORE [r5], r1 | C. XOR r1, r1, r1 | 36 |
|--------------------------|-------------------|-----------------------|----|
| D. ADD r1, r5, r1 | E. SUB r2, r1, r4 | F. LOAD r5, [r2] | 38 |
| G. SUB r4, r4, r4 | H. XOR r6, r6, r6 | I. ADD r4, r4, 0x1234 | 40 |

Figure 2A (Prior Art)

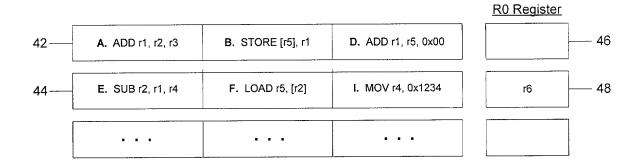


Figure 2B